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** CONTINUING DATA *****

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** FOREIGN APPLICATIONS *****

JAPAN 2002-313530 10/28/2002 A.O.A.

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35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Met after Allowance				
Verified and Acknowledged Examiner's Signature _____ Initials _____				

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TITLE

Semiconductor device and chip-stack semiconductor device

FILING FEE RECEIVED 918	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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